

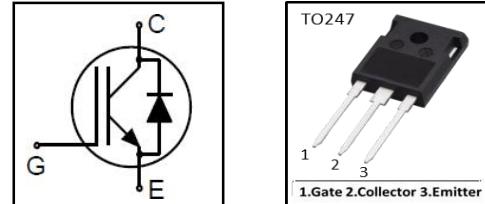
Features

- Ultra-low $V_{CE(sat)}$
- High Surge Current
- High Ruggedness

Product Summary	
V_{CES}	650V
I_C	50A ⁽¹⁾
$V_{CE(sat),typ.}$	1.65V ($T_J = 25^\circ C$)
Package	TO-247

Applications

- Solar photovoltaic inverters
- Uninterruptible power supplies (UPS)
- Inner IGBTs for NPC inverters
- Neutral-point IGBTs for T-type inverters



Package Marking and Ordering Information

Device	Device Package	Device Marking
YX50N65IGBT	TO-247	YX50N65

Absolute Maximum Ratings ($T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Value	Units
V_{CES}	Collector- Emitter Voltage	650	V
V_{GES}	Gate- Emitter Voltage	± 30	V
I_C	Collector Current	100	A
	Collector Current @ $T_c = 100^\circ C$	50	A
I_{Cpuls}	Pulsed Collector Current , t_p limited by T_{jmax}	200	A
-	Turn off safe operating area , $V_{CE}=650V$, $T_j=150^\circ C$	200	A
I_F	Diode Continuous Forward Current @ $T_c = 100^\circ C$	50	A
I_{FM}	Diode Maximum Forward Current	300	A
P_D	Power Dissipation @ $T_c = 25^\circ C$	260	W
	Power Dissipation @ $T_c = 100^\circ C$	135	W
T_J, T_{stg}	Operating Junction and Storage Temperature Range	-55 to + 175	°C
T_L	Maximum Temperature for Soldering	260	°C
t_{sc}	Short circuit withstand time $V_{GE}=15V$, $V_{CC}\leqslant 400V$, Allowed number of short circuits<1000 Time between short circuits: $\geqslant 1.0s$, $T_j\leqslant 150^\circ C$	5	us

Thermal Characteristic

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance, Junction to case for IGBT	0.48	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction to case for Diode	1.1	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	40	°C/W

Electrical Characteristics ($T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Conditions	Value			Units
			Min.	Typ.	Max.	
Static Characteristics						
$V_{(BR)CES}$	Collector- Emitter Breakdown Voltage	$V_{GE}=0V, I_{CE}=1mA$	650	--	--	V
I_{CES}	Collector- Emitter Leakage Current	$V_{GE} = 0V, V_{CE}=650V$	--	--	40	uA
$I_{GES(F)}$	Gate to Emitter Forward Leakage	$V_{GE}=+20V, V_{CE}=0V$	--	--	100	nA
$I_{GES(R)}$	Gate to Emitter Reverse Leakage	$V_{GE}=-20V, V_{CE} = 0V$	--	--	100	nA
$V_{CE(sat)}$	Collector- Emitter Saturation Voltage	$I_C=50A$	$T_j=25^\circ C$	--	1.65	V
		$V_{GE}=15V$	$T_j=150^\circ C$	--	2.05	V
$V_{GE(th)}$	Gate Threshold Voltage	$I_C=1mA, V_{CE}=V_{GE}$	4.0	5.0	6.0	V
Dynamic Characteristics						
C_{ies}	Input Capacitance	$V_{CE}=30V, V_{GE}=0V, f=1MHz$	--	3800	--	pF
C_{oes}	Output Capacitance		--	130	--	
C_{res}	Reverse Transfer Capacitance		--	70	--	
Q_g	Total Gate Charge	$V_{CC}=520V, I_C=50A, V_{GE}=15V$	--	162	--	nC
Q_{ge}	Gate to Emitter Charge		--	--	--	
Q_{gc}	Gate to Collector Charge		--	--	--	
$I_{C(SC)}$	Short circuit collector current Max. 1000 short circuits Time between short circuits: $\geq 1.0s$	$V_{GE}=15V, V_{CC}\leq 400V, t_{sc}\leq 5\mu s, T_j\leq 150^\circ C$	--	200	--	A
Switching Characteristics						
$t_{d(ON)}$	Turn-on Delay Time	$V_{CC}=400V, I_C=50A, V_{GE}=0/ 15V, R_g=5\Omega$, Inductive Load	--	60	--	ns
t_r	Rise Time		--	60	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	172	--	
t_f	Fall Time		--	90	--	
E_{on}	Turn-On Switching Loss		--	2.35	--	mJ
E_{off}	Turn-Off Switching Loss		--	0.82	--	
E_{ts}	Total Switching Loss		--	--	--	

Electrical Characteristics of the Diode ($T_c= 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Conditions	Rating			Units
			Min.	Typ.	Max.	
V_{FM}	Diode Forward Voltage	$I_F=50A$	--	1.65	2.0	V
T_{rr}	Reverse Recovery Time	$I_F=50A, di/dt=200A/us$	--	194	--	ns
I_{RRM}	Diode Peak Reverse Recovery Current		--	2.8	--	A
Q_{rr}	Reverse Recovery Charge		--	0.2	--	uC

Pulse width $t_{rp}\leq 380\mu s, \delta\leq 2\%$

Electrical Characteristics Graphs

Fig. 1 FBSOA characteristics

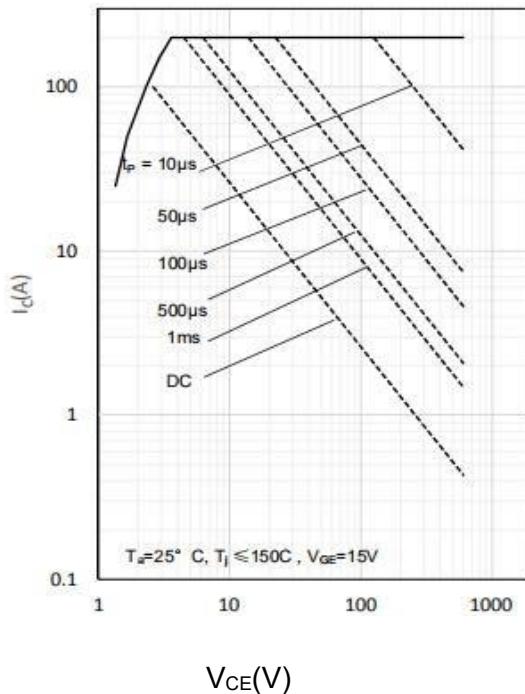


Fig. 2 Power dissipation as a function of TC

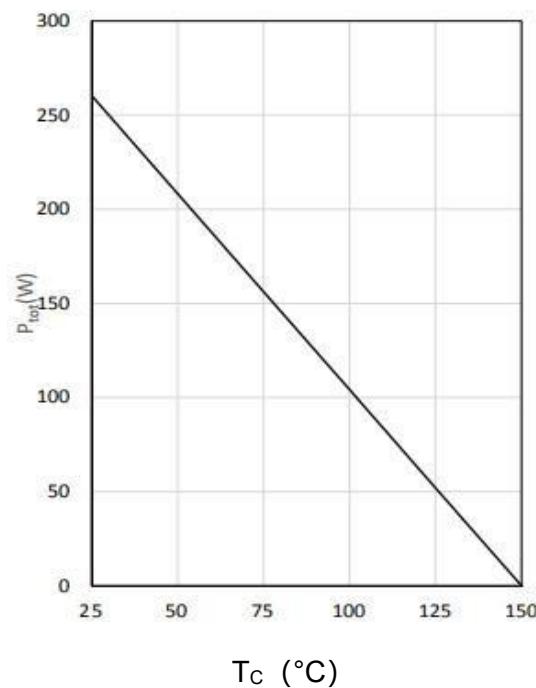


Fig. 3 Output characteristics

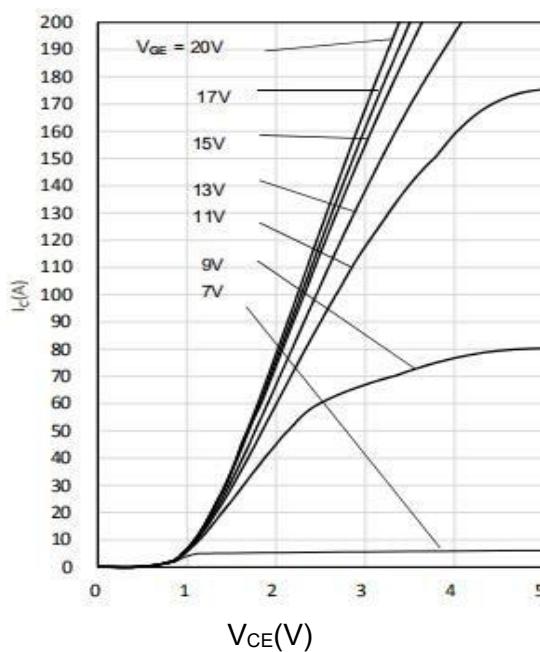
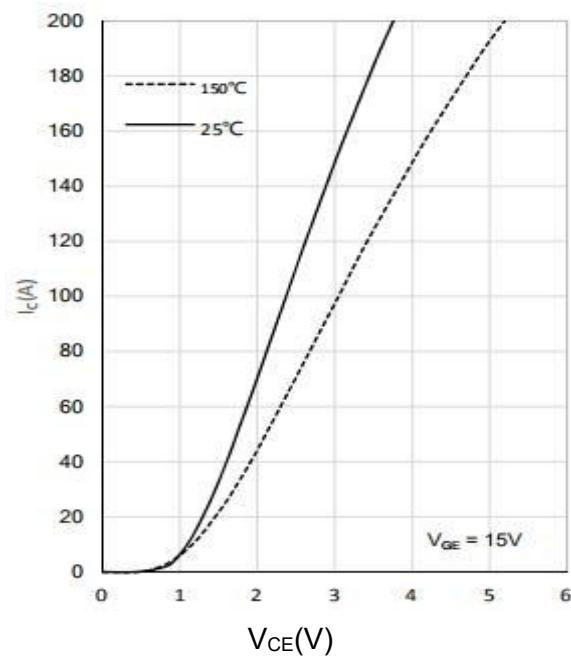


Fig. 4 Saturation voltage characteristics





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Fig. 5 Switching times vs. gate resistor

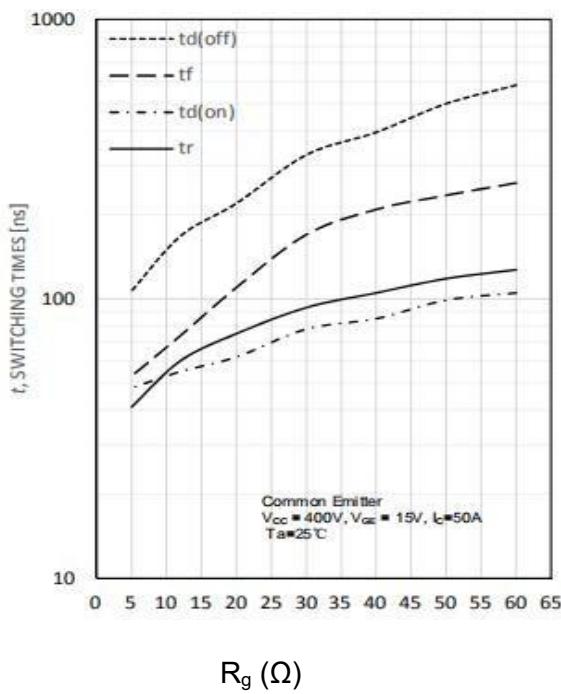


Fig. 6 Switching times vs. collector current

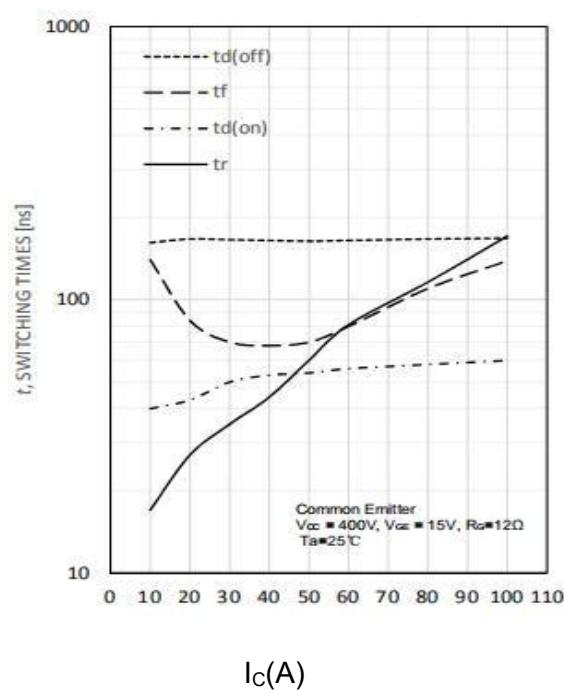


Fig. 7 Switching loss vs. gate resistor

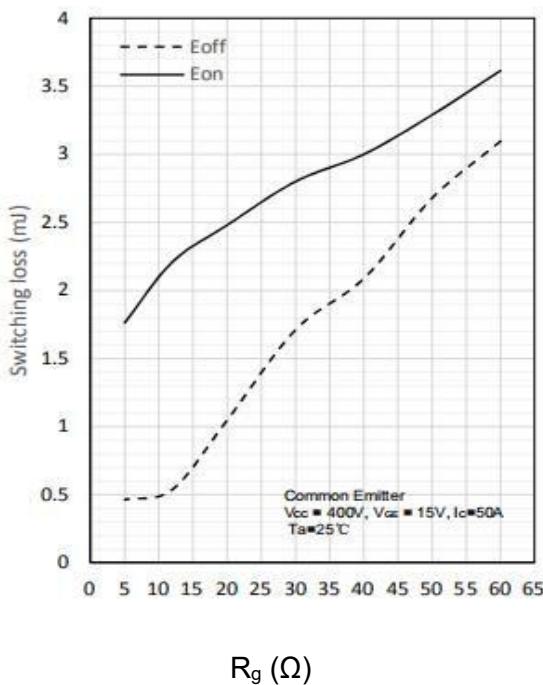


Fig. 8 Switching loss vs. collector current

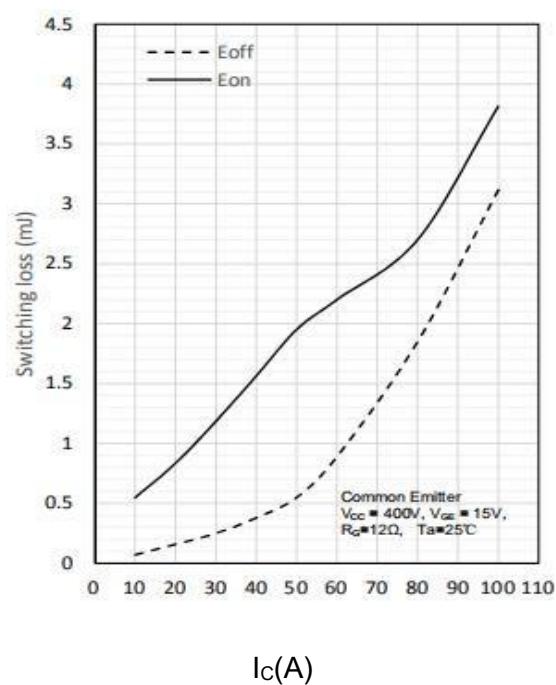


Fig. 9 Gate charge characteristics

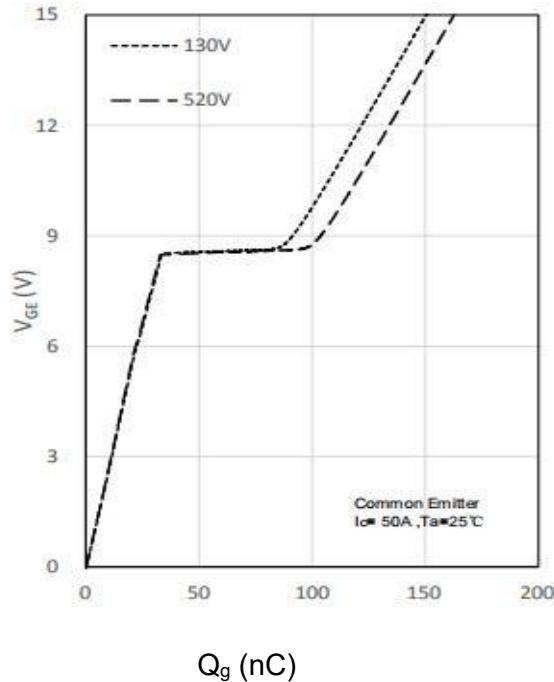
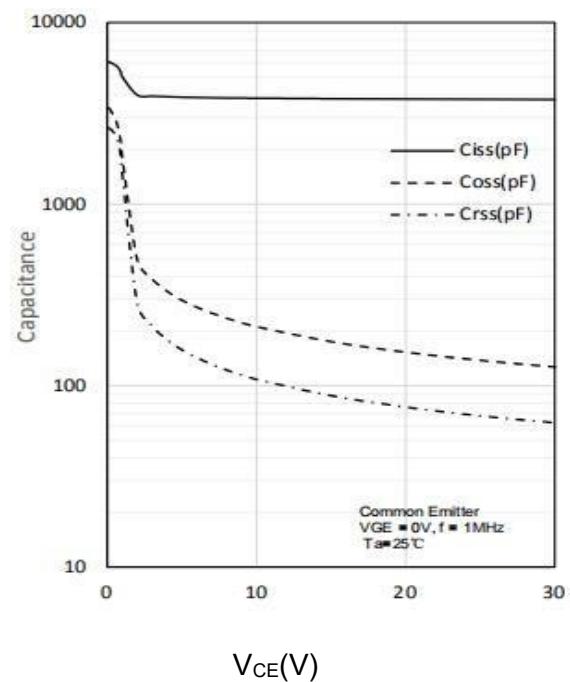


Fig. 10 Capacitance characteristics





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Package Drawing

